



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

## BIB DATA SHEET

CONFIRMATION NO. 6745

<b>SERIAL NUMBER</b> 10/639,465	<b>FILING or 371(c) DATE</b> 08/13/2003 <b>RULE</b>	<b>CLASS</b> 438	<b>GROUP ART UNIT</b> 1792	<b>ATTORNEY DOCKET NO.</b> 501.39812VV2
<b>APPLICANTS</b> Kazuo Yamazaki, Kokubunji, JAPAN; Shinji Kuniyoshi, Tokyo, JAPAN; Kousuke Kusakari, Hitachinaka, JAPAN; Takenobu Ikeda, Ome, JAPAN; Masahiro Tadokoro, Hachioji, JAPAN;				
<b>** CONTINUING DATA *****</b> This application is a DIV of 09/811,589 03/20/2001 PAT 6,633,072 which is a DIV of 09/810,577 03/19/2001 PAT 6,479,392				
<b>** FOREIGN APPLICATIONS *****</b> JAPAN 2000-094986 03/30/2000				
<b>** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **</b> 11/08/2003				
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and /GEORGE A GOUDREAU/ Acknowledged Examiner's Signature	<input type="checkbox"/> Met after Allowance Initials	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWINGS</b> 21	<b>TOTAL CLAIMS</b> 29
<b>INDEPENDENT CLAIMS</b> 3				
<b>ADDRESS</b> ANTONELLI, TERRY, STOUT & KRAUS, LLP 1300 NORTH SEVENTEENTH STREET SUITE 1800 ARLINGTON, VA 22209-3873 UNITED STATES				
<b>TITLE</b> Fabrication method for semiconductor integrated circuit device				
<b>FILING FEE RECEIVED</b> 1012	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	